



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) :

Kenneth Shepard

Serial No.

09/591,270

Examiner

Ayal I. Sharon

Filed

June 9, 2000

Art Unit

2123

For

METHODS FOR ESTIMATING THE BODY VOLTAGE OF

DIGITAL PARTIALLY DEPLETED SILICON-ON-

INSULATOR CIRCUITS

RECEIVED

DEC 1 2 2003

Technology Center 2100

DECLARATION PURSUANT TO 37 C.F.R. §1.131

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450

Date of Deposit

Paul A. Ragusa Attorney Name 38,587

PTO Registration No

Signature

Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450 Dear Sir:

I, Kenneth L. Shepard, hereby declare as follows:

AP32158 - 70050.1280

1. I am the inventor of the inventions as recited in claims 1-8 and 12-22 of the above-

identified patent application;

2. The inventions of claims 1-8 and 12-22 of the above-identified patent application,

among others, were conceived and reduced to practice in this country, prior to April

19, 1999 (Henceforth referred to as the "Critical Date"), which is the filing date of

Chuang et al. U.S. Patent Application Publication No. US2003/0078763 A1;

3. Prior to the Critical Date, I made an original submission of the inventions to the

International Conference on Computer-Aided Design (ICCAD) on April 9, 1999.

Attached hereto as Exhibit A is a copy of the submission. The date is not shown on

this submission. However, the UNIX time-date stamp shows the date as April 9,

1999. Additionally, the review submission deadline for ICCAD 1999 was April 9,

1999;

4. Also prior to the Critical Date, I made a presentation of the inventions. Attached

hereto as Exhibit B is a copy of the presentation. The date is not shown on this copy.

However, the time-date stamp of this presentation was February 8, 1999;

5. I declare further that all statements made herein of my own knowledge are true and

that all statements made on information and belief are believed to be true, and further

that these statements were made with the knowledge that willful false statements and

the like so made are punishable by fine or imprisonment, or both, under Section 1001

of Title 18 of the United States Code and that such willful statements may jeopardize

the validity of the application or any patent issuing therefrom.

Date: 11 (17/2003

KENNETH L. SHEPARD

Dennett Slegger